

What are on-chip energy-storage devices?

On-chip energy-storage devices play an important role in powering wireless environmental sensors and micro-electromechanical systems [1,2]. Starting from the 1980s,on-chip energy-storage devices,including micro-batteries and supercapacitors,have been applied to power the real-time clock on a chip [3].

How effective is on-chip energy storage?

To be effective, on-chip energy storage must be able to store a large amount of energy in a very small space and deliver it quickly when needed - requirements that can't be met with existing technologies.

Could on-Microchip energy storage change the world?

Their findings, reported this month in Nature, have the potential to change the paradigm for on-microchip energy storage solutions and pave the way for sustainable, autonomous electronic microsystems.

What is the in-transistor energy-storage chip model?

To answer this question,Mai,Yan and colleagues designed an in-transistor energy-storage chip model (Mai-Yan model),as shown in Fig. 1. Interestingly,the charge-storage capability is amplified by a parameter in transistors,named the gate voltage.

Do nanostructured storage devices increase capacitance density?

Nanostructured storage devices with 3D metal-insulator-metal (MIM) architectures--which require conformal metal and insulator deposition inside porous nanostructures--have successfully increased capacitance density,and therefore energy storage,per unit planar area (Fig. 3b,Supplementary Table 3).

The growing demand for high-power-density electric and electronic systems has encouraged the development of energy-storage capacitors with attributes such as high energy density, high capacitance ...

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Advanced Materials 31 (25), 2019, 1807450. The development of self-powered electronic systems requires integration of on-chip energy-storage units to interface with various types of energy harvesters, which are ...

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